APPROVED BY DRAFTSMAN
O.G. FIG. 5
CLASS 714
SUBCLASS 014823-116 724

6185707

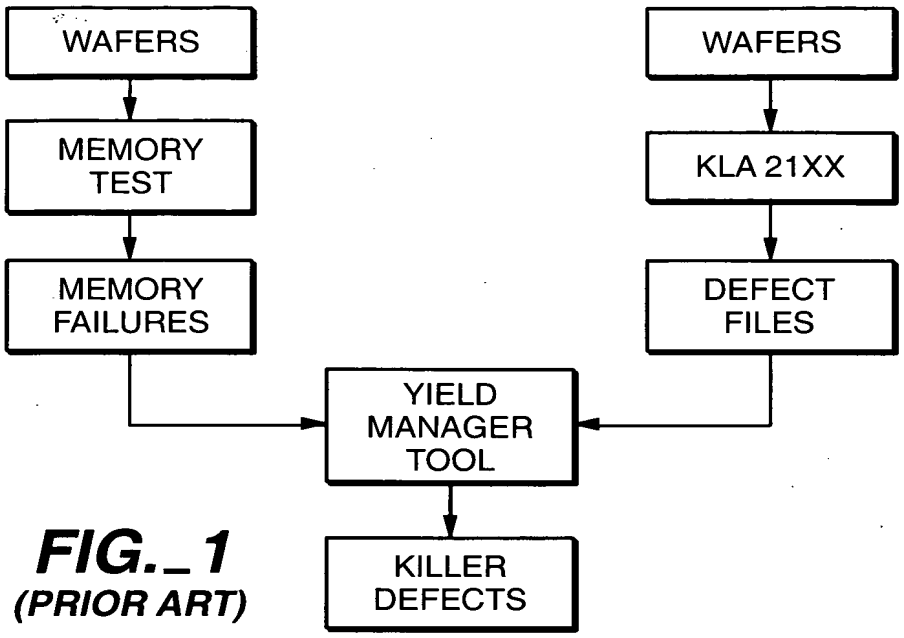

FIG._1
(PRIOR ART)

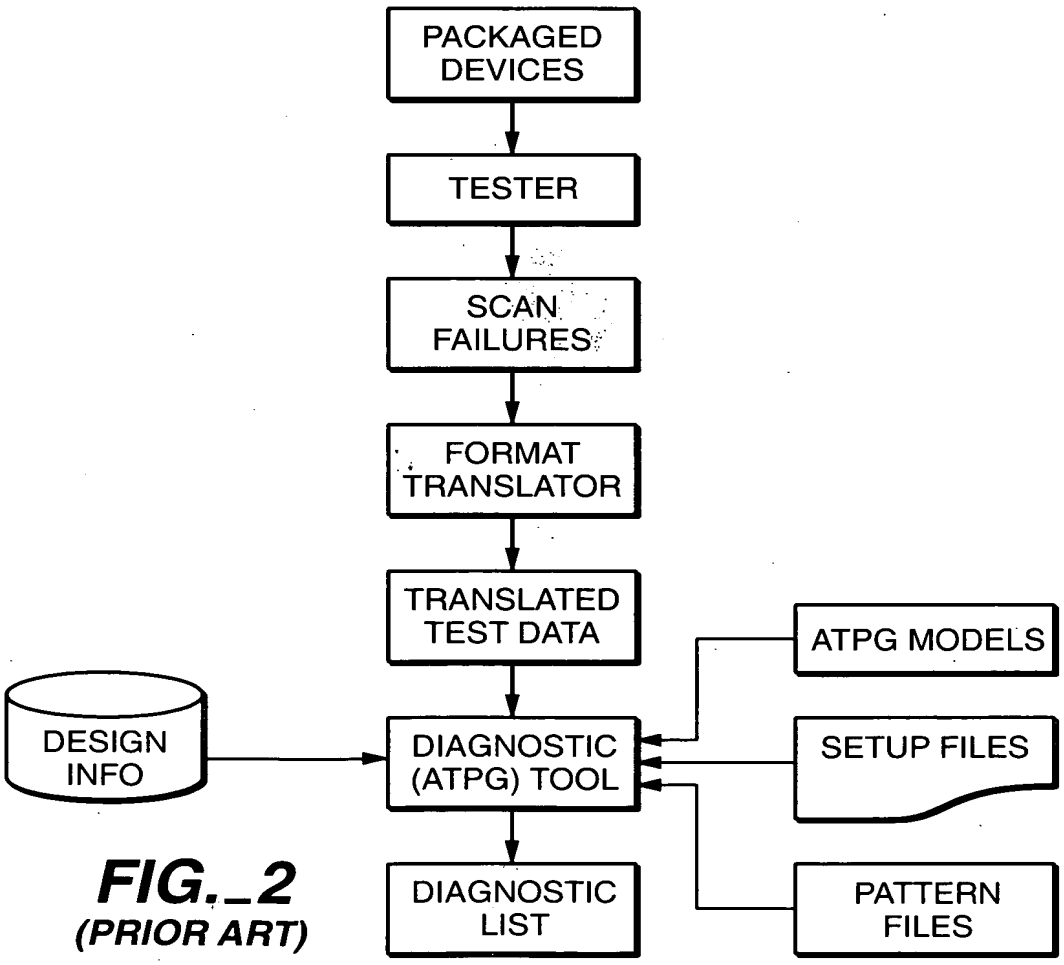

FIG._2
(PRIOR ART)

```
DeviceID = DEMOID
LotID = TESTLOT01
WaferID = 01
DIE = -4,0
datalog.scan.10 diagnosis summary, #failing_patterns=9  #defects=2
#unexplained_fails=2
unexplained patterns = 212 250
-------------------------------------------------------------------------
fault candidates for defect 1,  #failing_patterns_explained=5
-------------------------------------------------------------------------
Warning: Fault candidates will cause passing patterns to fail.
failing patterns explained = 322 706 738 770
type   code   pin_pathname
----   ----   ------------------------------------
  1    DS     /XTIO_0/XTTLI8_1612/N2_23
  1    DS     /PI9
-------------------------------------------------------------------------
diagnosis CPU time = .68 sec
```

FIG._3
(PRIOR ART)

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O.G. FIG.
714
784

APPROVED BY DRAFTSMAN
O.G. FIG. 5
CLASS/SUBCLASS
714 724

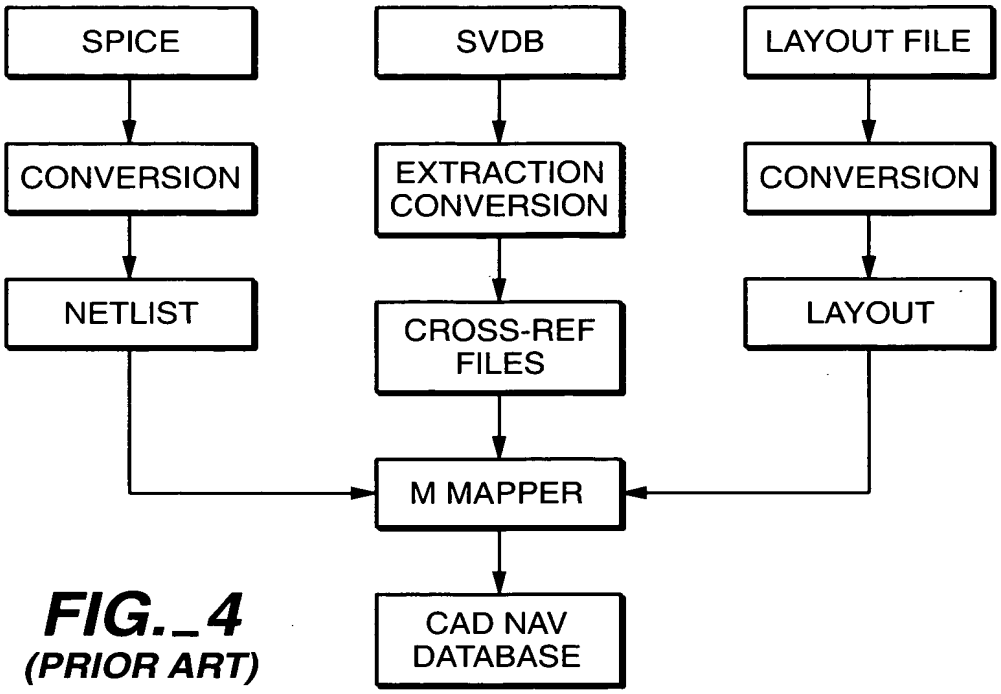

FIG._4
(PRIOR ART)

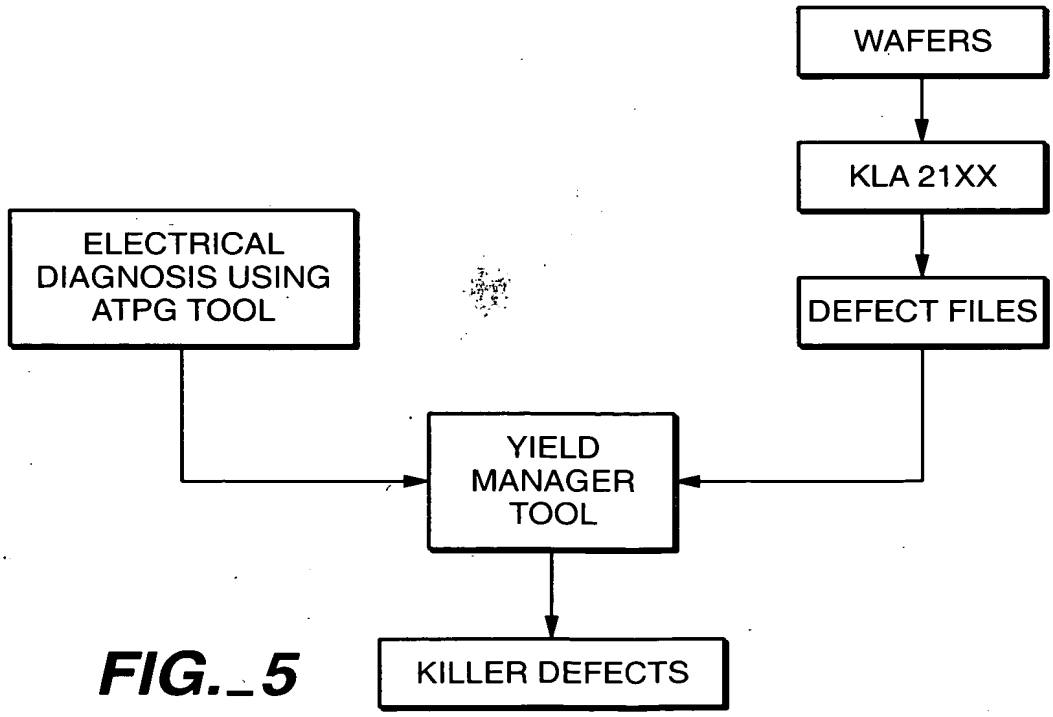

FIG._5

APPROVED BY DRAFTSMAN | O.G. FIG. | CLASS | SUBCLASS
714 | 724

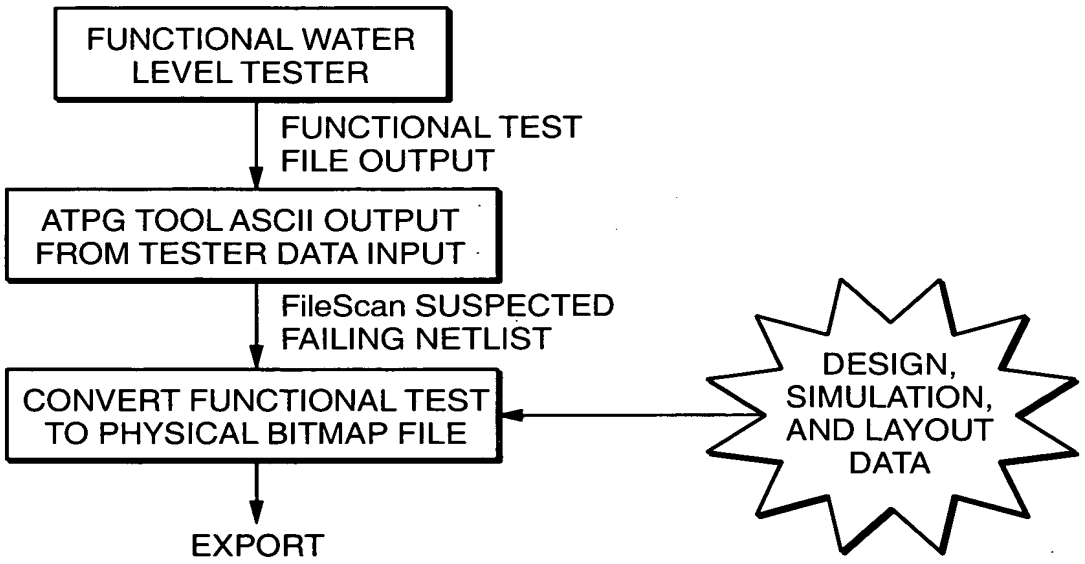

FIG._6

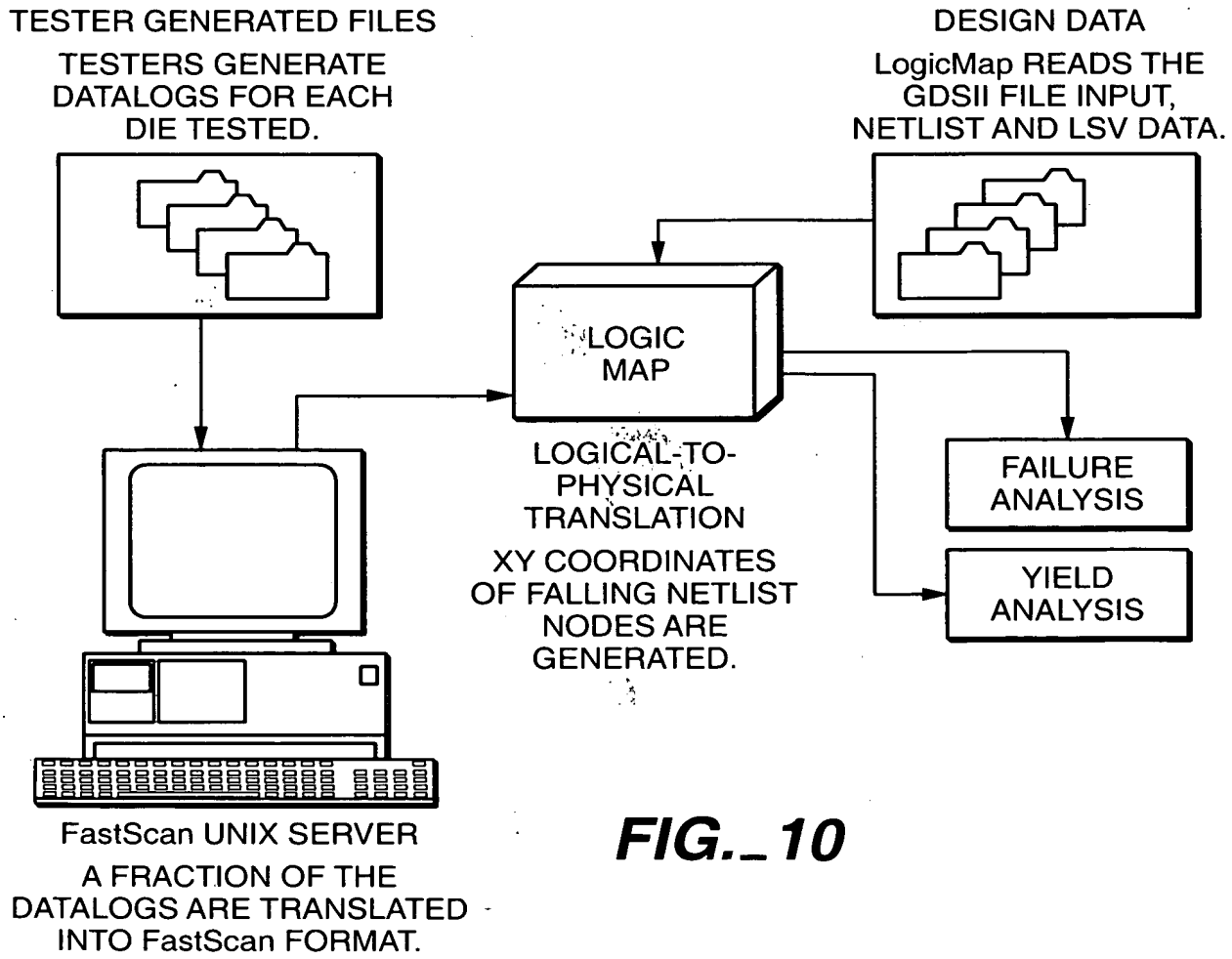

FIG._10

APPROVED BY DRAFTSMAN
O.G. FIG.
CLASS 714 SUBCLASS 724

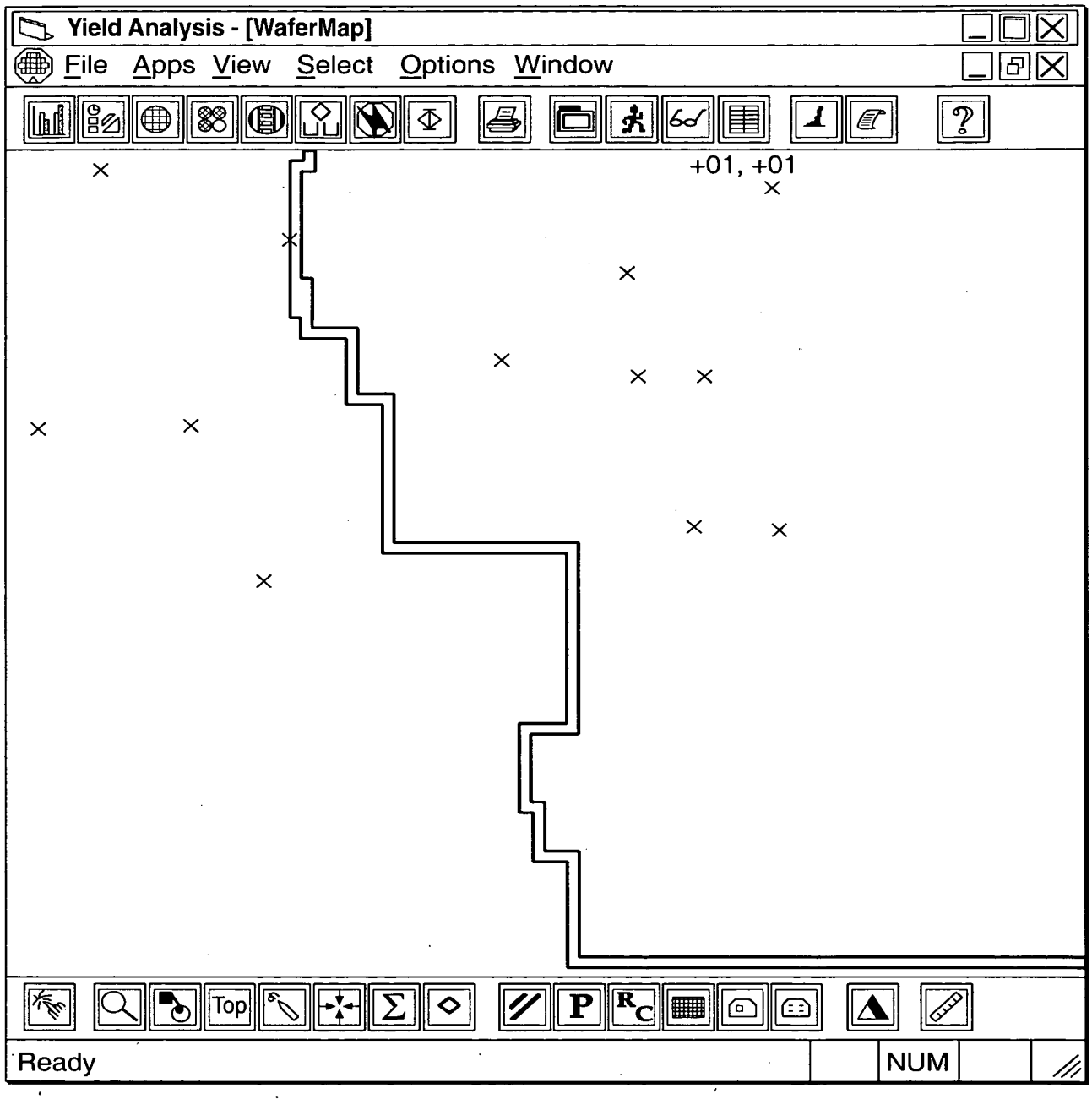

FIG._7

APPROVED BY DRAFTSMAN
O.G. FIG.
CLASS SUBCLASS
714 784

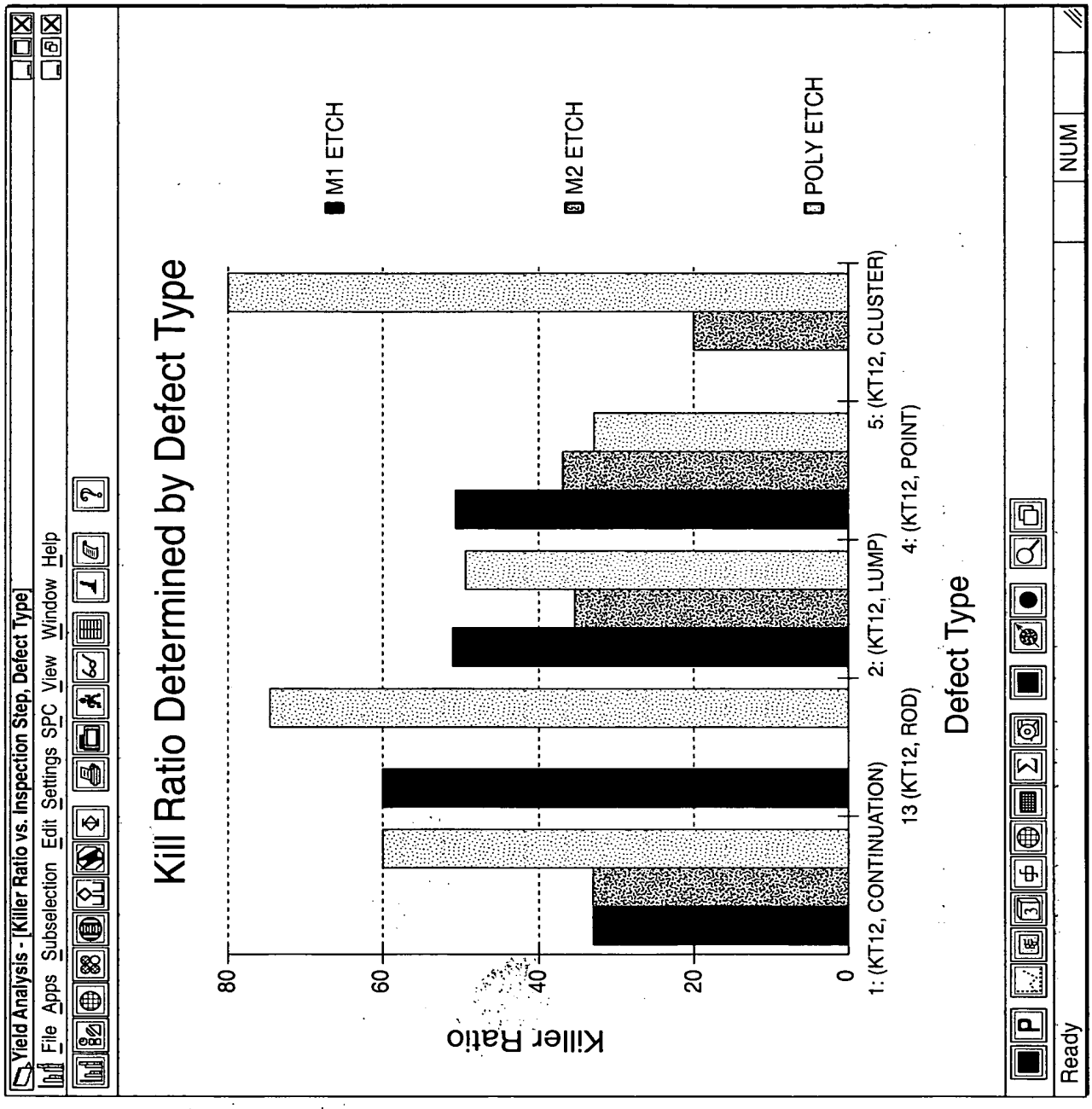

FIG._8

APPROVED BY DRAFTSMAN | O.G. FIG. | CLASS | SUBCLASS
714 | 714 | 724

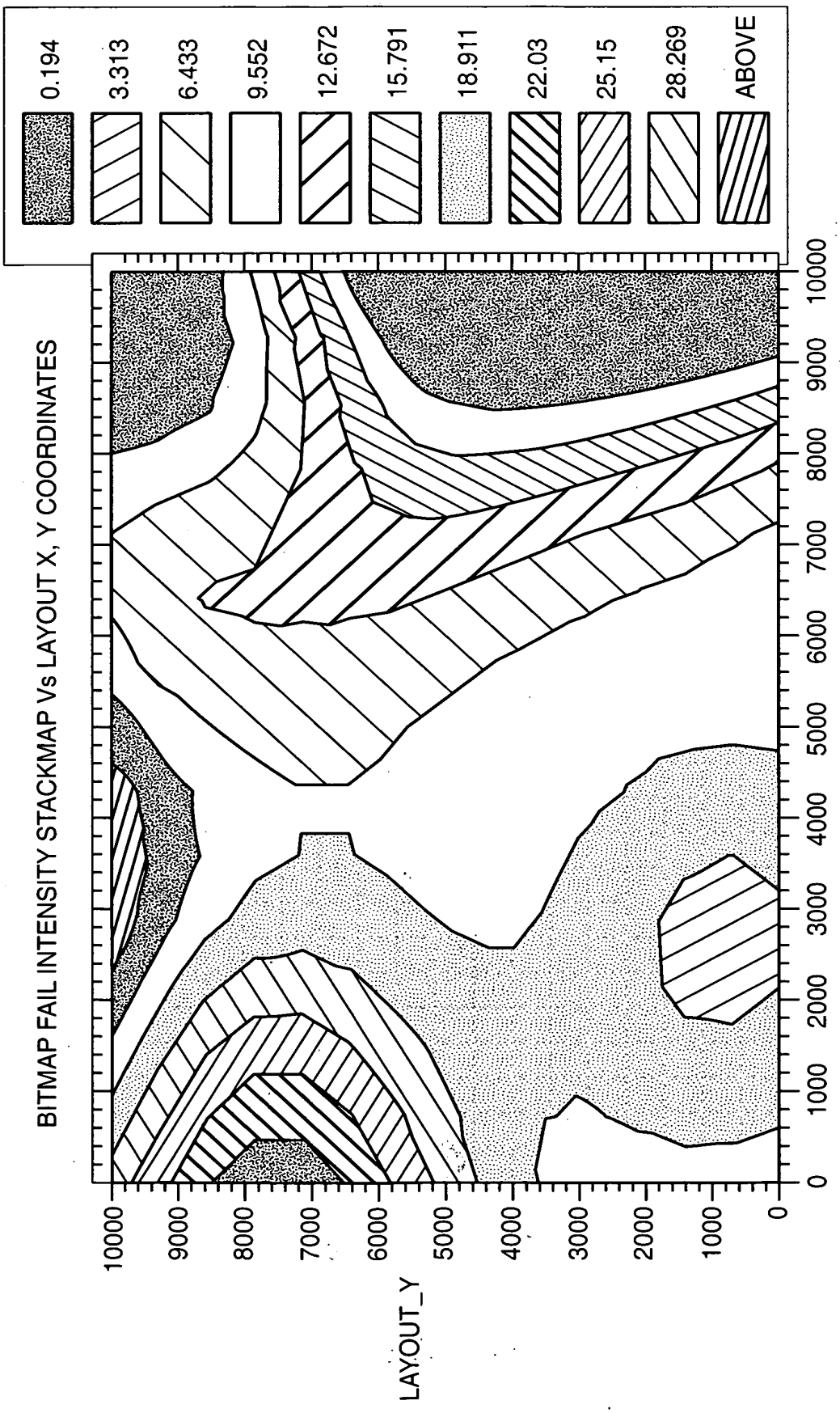

FIG._9

APPROVED BY 01/08/03
O.G. FIG.
CLASS SUBCLASS
DRAFTSMAN 7/4 73

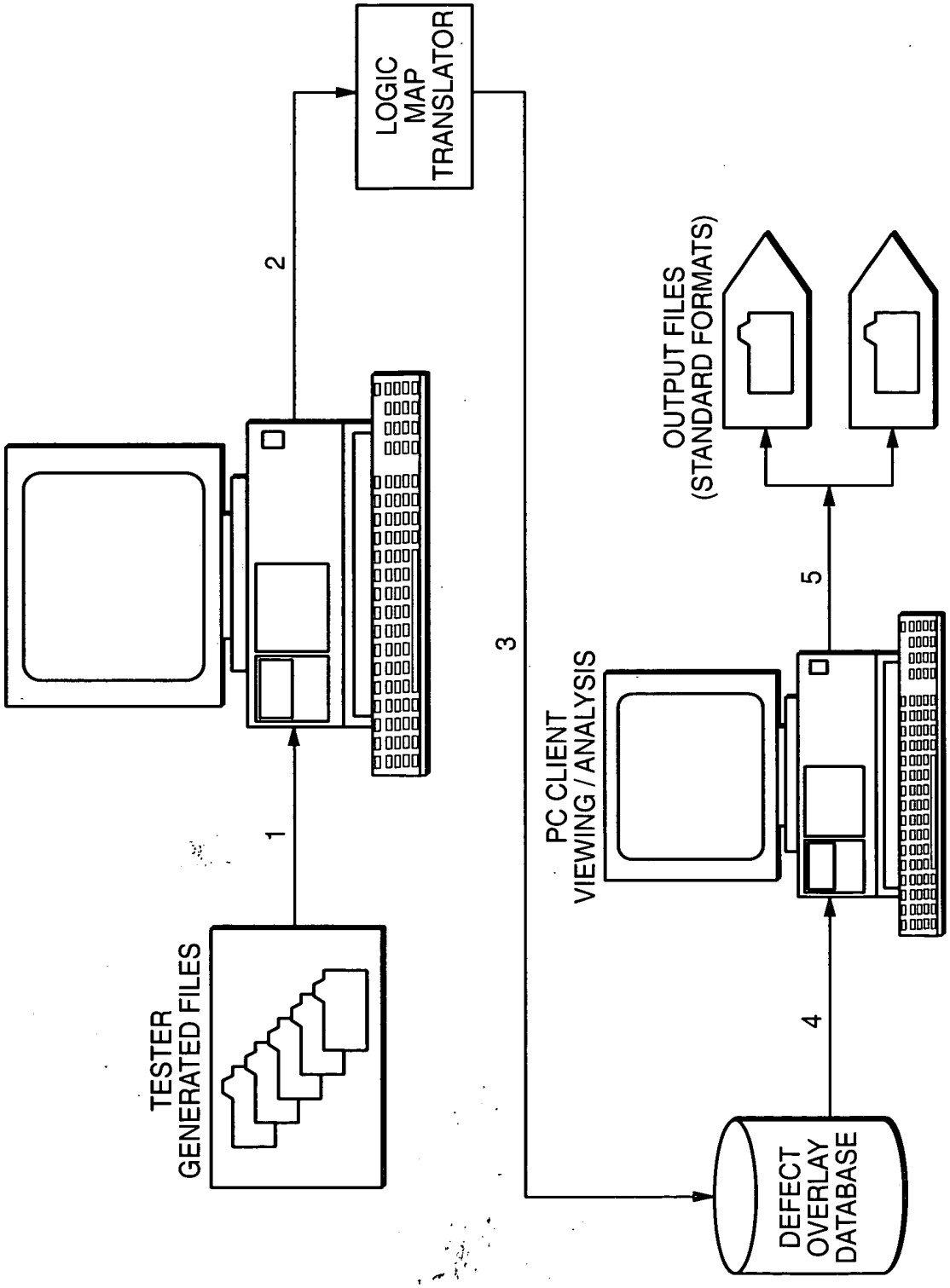

FIG._11

APPROVED BY DRAFTSMAN | O.G. FIG. | CLASS | SUBCLASS
714 | 724

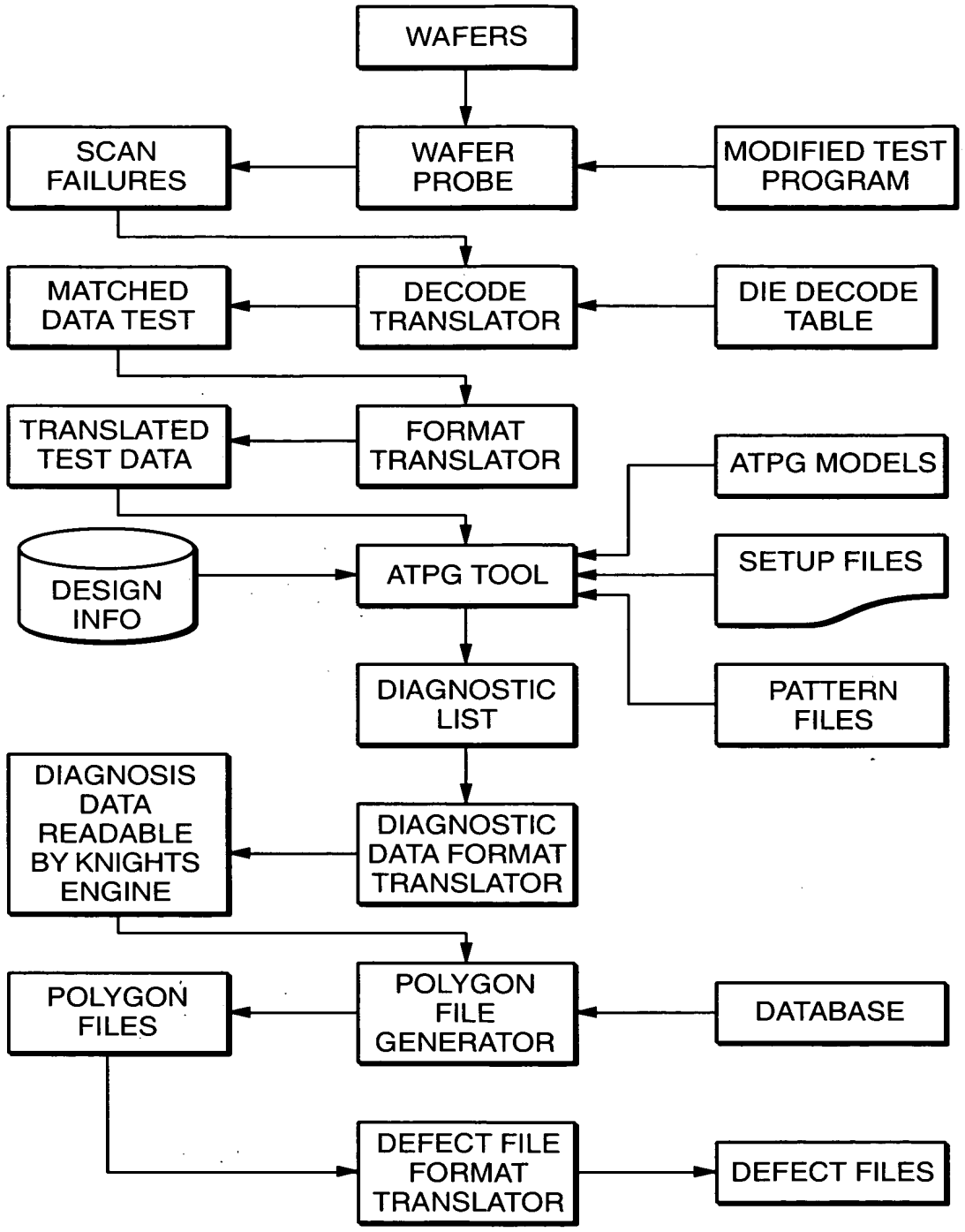

FIG._12